

CAMAC

16ch TDC

(C021)



- ◎ TAC pattern TDC of the time resolution
- ◎ 12bit Full scale data.
(200nSec Standard)
- ◎ Common start mode.
- ◎ All Input signals are NIM level.

《CAMAC Function》

F (0)	· A (0 ~ 15)	· S 1	: Data Read 0 ch ~ 15 ch
F (2)	· A (0 ~ 15)	· S 1	: Data Read 0 ch ~ 15 ch
F (2)	· A (15)	· S 2	: Data Clear
F (8)			: Test LAM
F (9)	· S 2		: Data Clear
F (10)	· S 2		: LAM Clear
C	· S 2		: Data Clear
Z	· S 2		: Data Clear

< Specifications >

Stop Input Signal	:	NIM Level Signal >20nSec width
Start Input Signal	:	NIM Level Signal >20nSec width
Time resolution	:	50pSec (Standard) It can be chaged 25pSec~1nSec (At the time of the order, designation)
A/D	:	12bit / 10 μ Sec conversion
Reality	:	Less than ± 2 counts
Form	:	CAMAC Standard 1 width module
Power supply	:	+6V 2A 、 -6V 2A -24V 1A 、 +24V 1A